Atty Docket No. ALNC-9400 [P6071]

DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

DELAY SETTINGS FOR A WIDE-RANGE, HIGH-PRECISION DELAY-LOCKED LOOP AND A DELAY LOCKED LOOP IMPLEMENTATION USING THESE SETTINGS

the specification of which (and was amended on	check one) X is attached (if applicable).	hereto or was filed on a	s Application No
I hereby state that I have reincluding the claims, as am	eviewed and understand the ended by any amendment i	e contents of the above-identified referred to above. ch is material to patentability as	
application(s) for patent or designated at least one cou	inventor's certificate, or § and the United patent or inventor's certific	. § 119(a)-(d) or § 365(b) of any 365(a) of any PCT International a States, listed below and have als ate having a filing date before tha	pplication which to identified below
Prior Foreign Application(s)			Priority Claimed Yes No
Number	Country	Day/Month/Year Filed	
Number .	Country	Day/Month/Year Filed	
I hereby claim the benefit u	nder 35 U.S.C. § 119(e) of	any United States provisional ap	plication(s) below.
Application Number	Filing Date		
Application Number	Filing Date		
PCT International application of each of the claims of this provided by the first paragrawhich is material to patenta date of the prior application	n designating the United St s application is not disclose aph of 35 U.S.C. § 112, I a bility as defined in 37 CFR	ny United States application(s), or ates, listed below and, insofar as d in the prior United States applications the disclose § 1.56 which became available beternational filing date of this applications.	the subject matter cation in the manner all information between the filing
Application Number	Filing Date	Status: Patented, Pending	g, Abandoned

Application Number	Filing Date	Status: Patented, Pending, Abandoned
made on information and belief ar the knowledge that willful false st	e believed to be true; a tatements and the like s d that such willful false	wn knowledge are true and that all statements nd further that these statements were made with so made are punishable by fine or imprisonment or statements may jeopardize the validity of the
Full name of sole or first inventor	CHAITANYA PALUSA	
Inventor's signature	~	12/07/00 Date
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Full name of second joint inventor	, if any, <u>ABHIJIT RAY</u>	
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